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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub-Class	Filing Date

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FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub-Class	Transl. Yes	Transl. No

OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

✓	Bertrand J. Williams et al., "LINEARIZED DIGITAL PHASE-LOCKED LOOP", U.S. Serial No. 09/745,660, Filed December 21, 2000.
✓	Bertrand J. Williams et al., "LINEARIZED DIGITAL PHASE-LOCKED LOOP METHOD", U.S. Serial No. 09/747,281, Filed December 21, 2000.
✓	Bertrand J. Williams et al., "LINEARIZED DIGITAL PHASE-LOCKED LOOP", U.S. Serial No. 09/747,262, Filed December 22, 2000.
✓	Timothy J. Jordan et al., "LINEARIZED DIGITAL PHASE-LOCKED LOOP METHOD", U.S. Serial No. 09/747,188 Filed December 22, 2000.
✓	Bertrand J. Williams et al., "LINEARIZED DIGITAL PHASE-LOCKED LOOP METHOD", U.S. Serial No. 09/746,802, Filed December 22, 2000.

Examiner	Date Considered
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